

Miniature HV Power Supplies

TF Series 1.25kV 0.7W

1111-02Aa



FEATURES

- Ultra-compact, PCB mountable
- Low ripple, 30mVp-p
- Low Noise due to metal shielding
- Well-regulated, high performance
- External potentiometer or external control voltage programming
- Arc and continuous short circuit protection

SUMMARY

TF Series are miniaturized, well regulated high voltage power supplies suitable for photomultiplier. They feature exceptionally low noise, external potentiometer or voltage control and fully protection against arc and continuous output short circuit.

Output voltage (kVdc)	Output current (mA) [☆]	Model		Ripple (mVp-p)
		Positive polar output	Negative polar output	
0 to 1.25	0.5	TF-1.25P-12	TF-1.25N-12	30
0 to 1.25	0.6	TF-1.25P-15	TF-1.25N-15	30

☆ Output voltage range for this output current is 50% to 100% of maximum output voltage.

☆ Output current must be derated linearly when operating at levels below 50% of the output voltage capability.

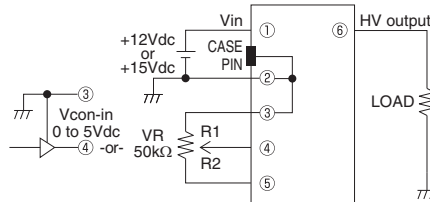
SPECIFICATIONS

Input voltage/current	(TF-1.25P-12, TF-1.25N-12) +12Vdc ±1Vdc, 95mA Typ (TF-1.25P-15, TF-1.25N-15) +15Vdc ±1Vdc, 90mA Typ
Output control	By external 50kΩ potentiometer or external control voltage (Vcon-in) 0 to 5Vdc (Input impedance ≥80kΩ)
Regulation	Line : ±0.01% of max voltage for Vin ±1V Load : 0.01% of max voltage for full load change
Stability	0.02%/Hr
Setting voltage accuracy	±0.5% (at maximum output Vcon-in)
Temperature coefficient	0.007%/°C
Protection	Reversible input connection Overload, arc and continuous output short circuit
Temperature range	Operating : -10°C to +50°C Storage : -25°C to +85°C
Weight	30g approx

Note : • Specifications are at the maximum rated output after ½Hr warm-up.

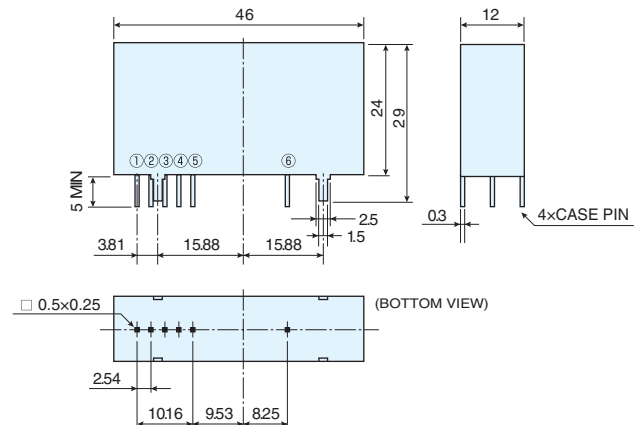
- Specifications are subject to change without notice.

CONNECTION DIAGRAM



1. PIN ②, ③ and case are internally connected.
2. CASE PIN should be always connected to ground.
3. Input impedance of Pin ④ is greater than 80kΩ
4. External potentiometer of T.C ≤ 100ppm/°C, PC ≥ ¼W is recommended.

DIMENSIONS mm



CHARACTERISTICS OF OUTPUT VOLTAGE SETTING

